

FIG. 1

AC DRIVING TYPE PLASMA DISPLAY APPARATUS

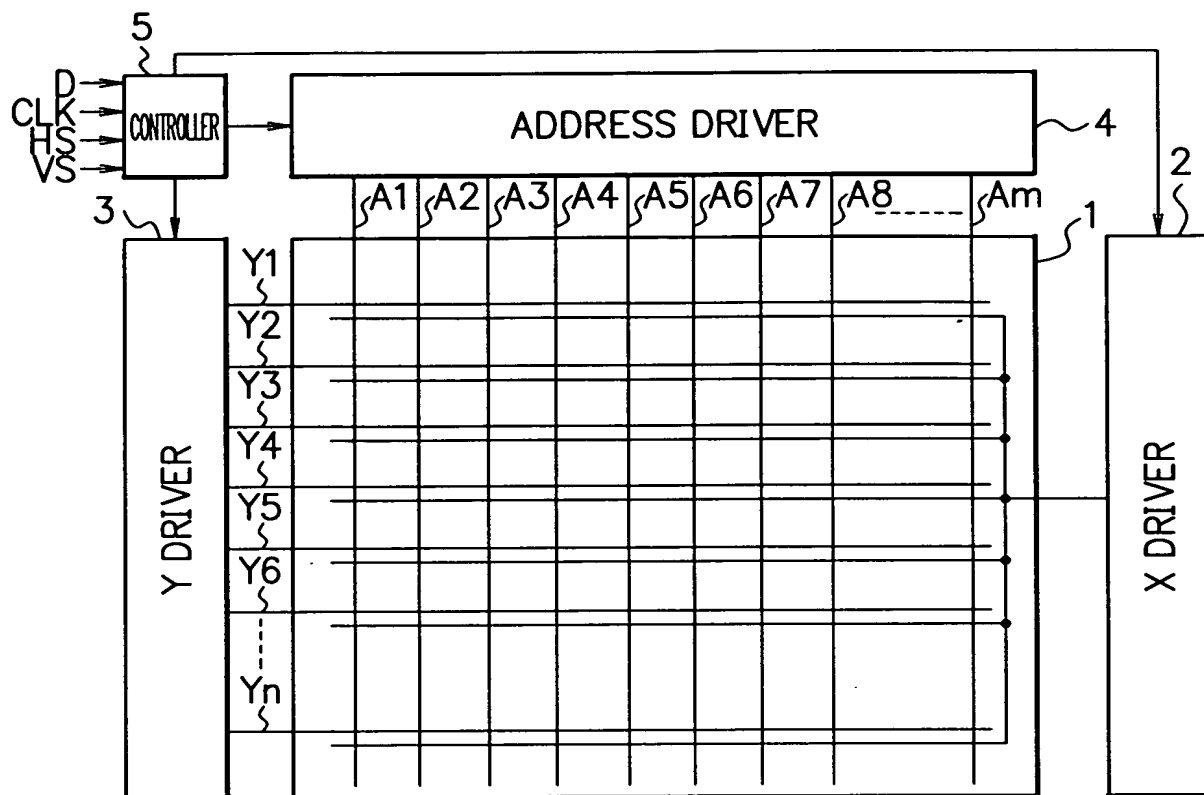
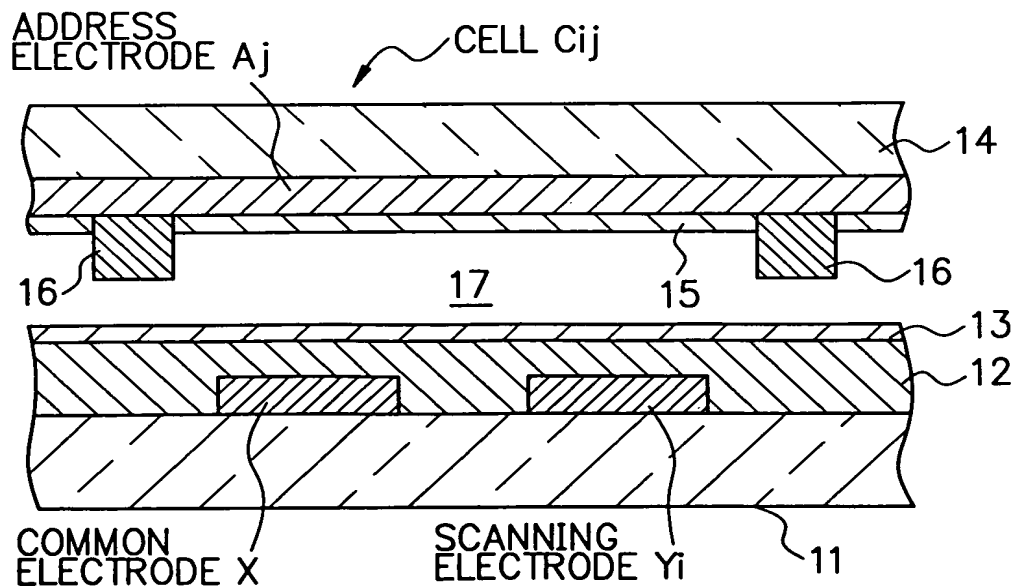


FIG. 2

SECTIONAL STRUCTURE OF CELL



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FIG. 3

WAVEFORM CHART SHOWING CONVENTIONAL DRIVING METHOD

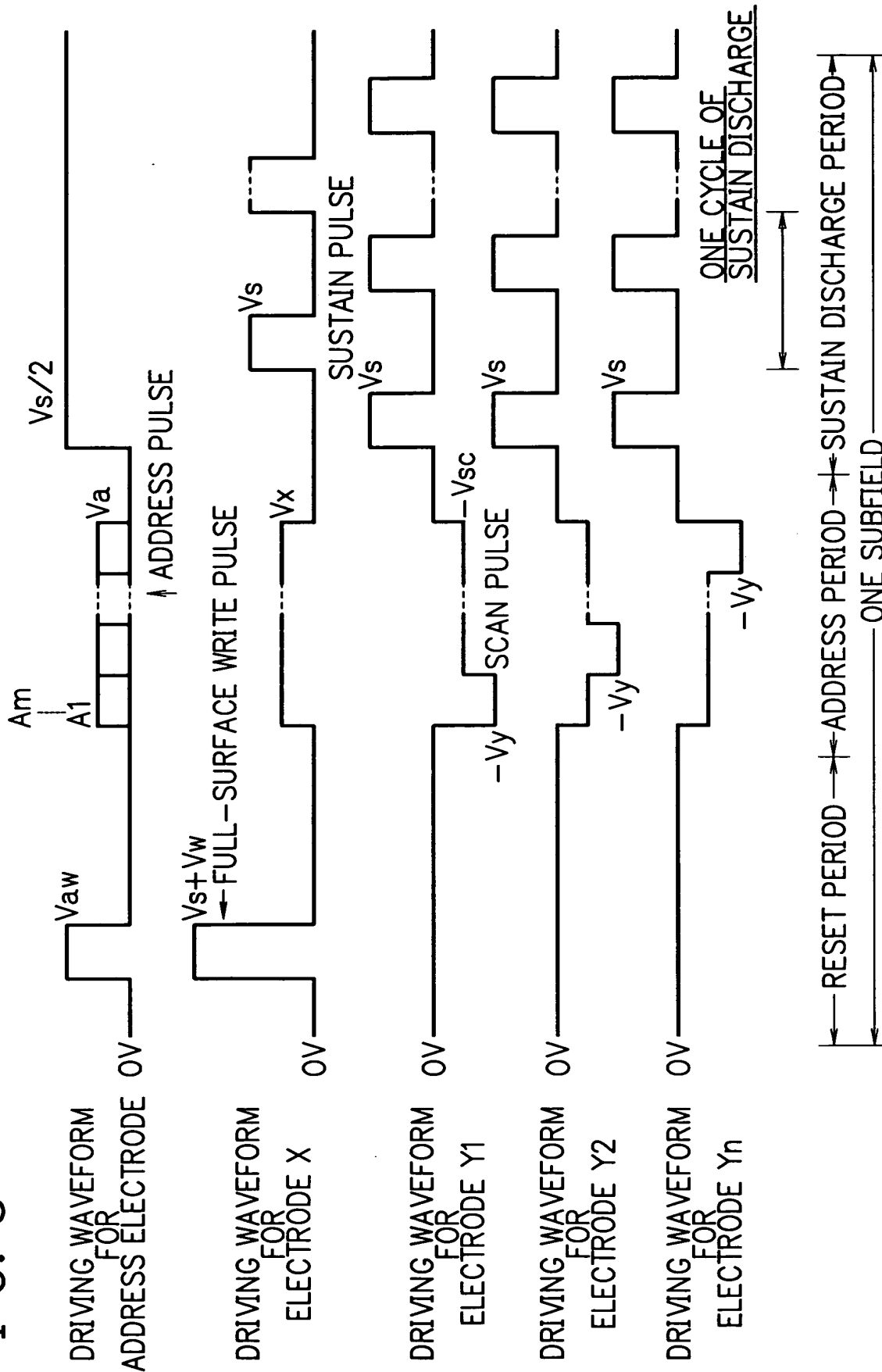


FIG. 4

CONVENTIONAL SUBFIELD STRUCTURE

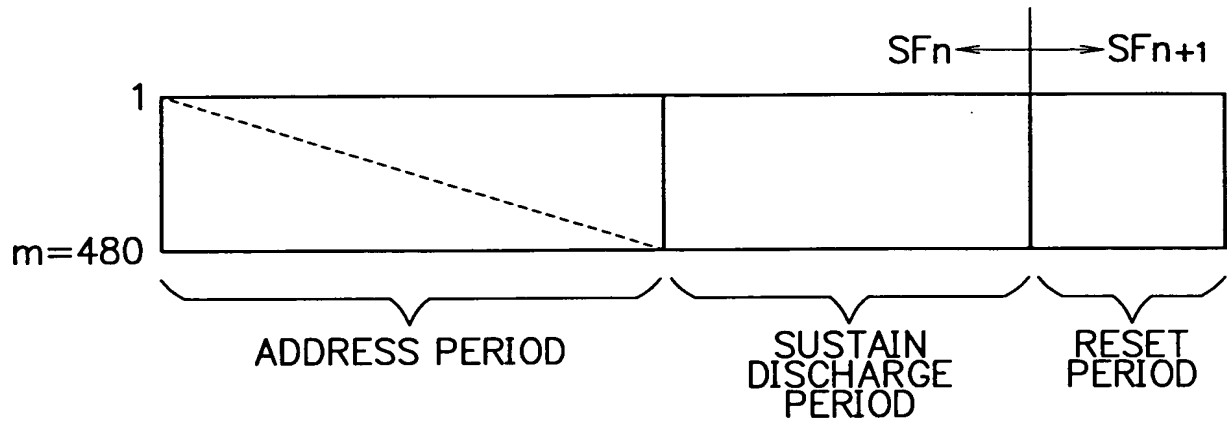
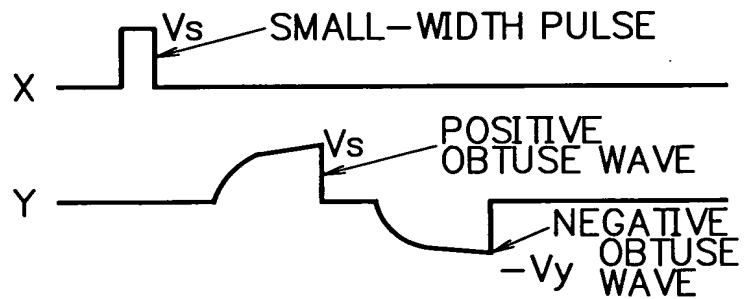


FIG. 5

WAVEFORM CHART SHOWING EXAMPLE OF PDP DRIVING METHOD



CHARGE ACCUMULATION STATE

FIG. 6A

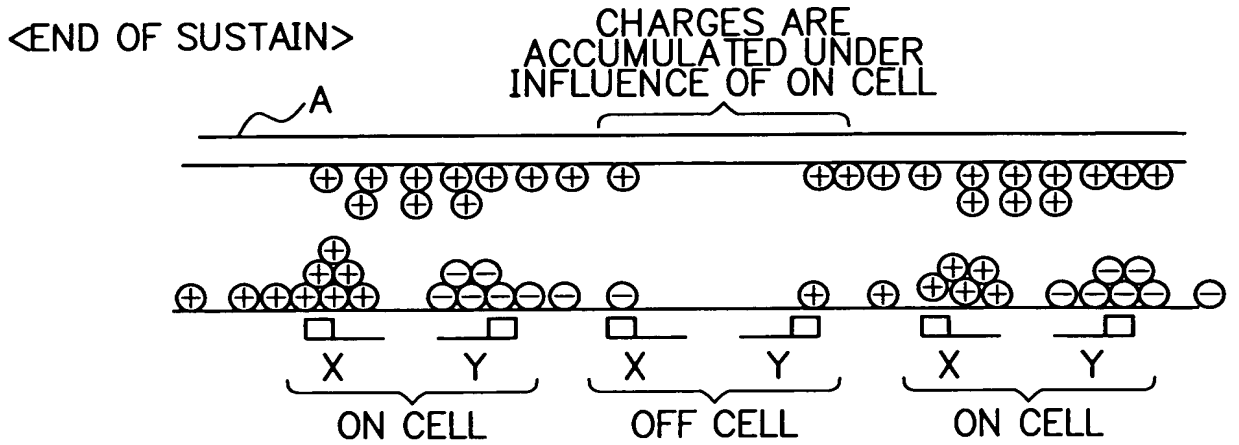


FIG. 6B

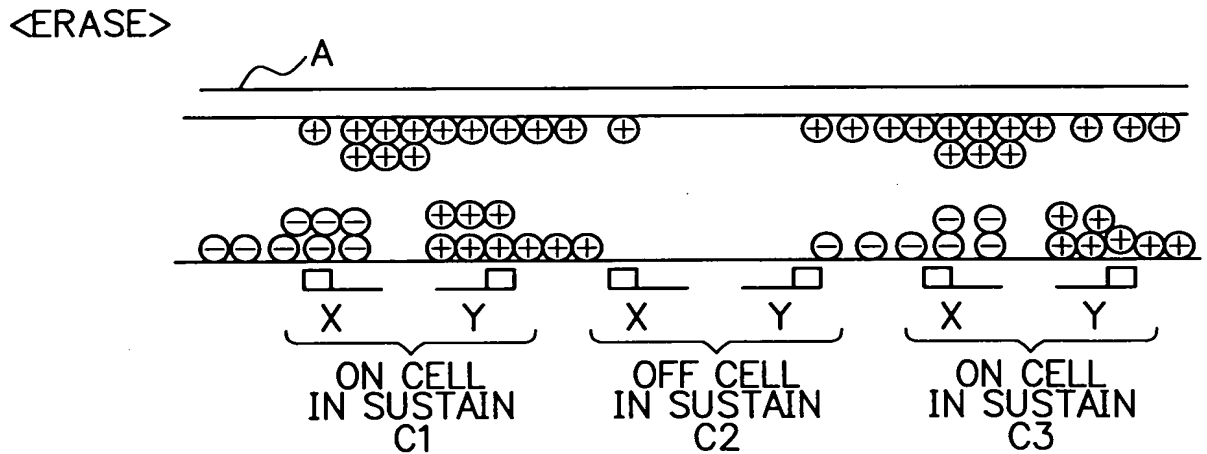


FIG. 6C

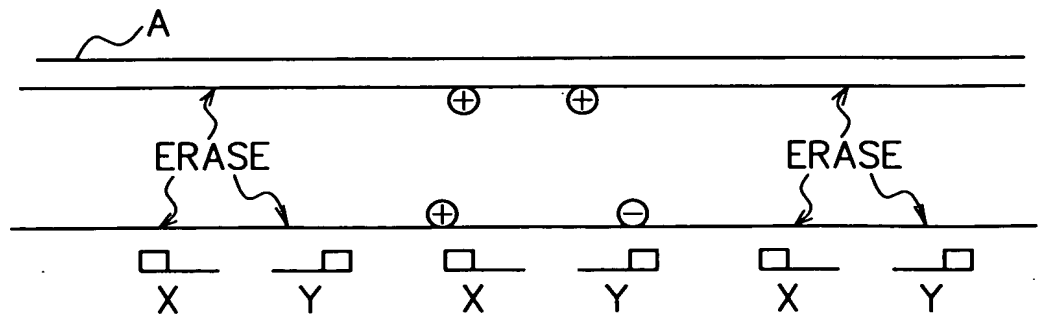
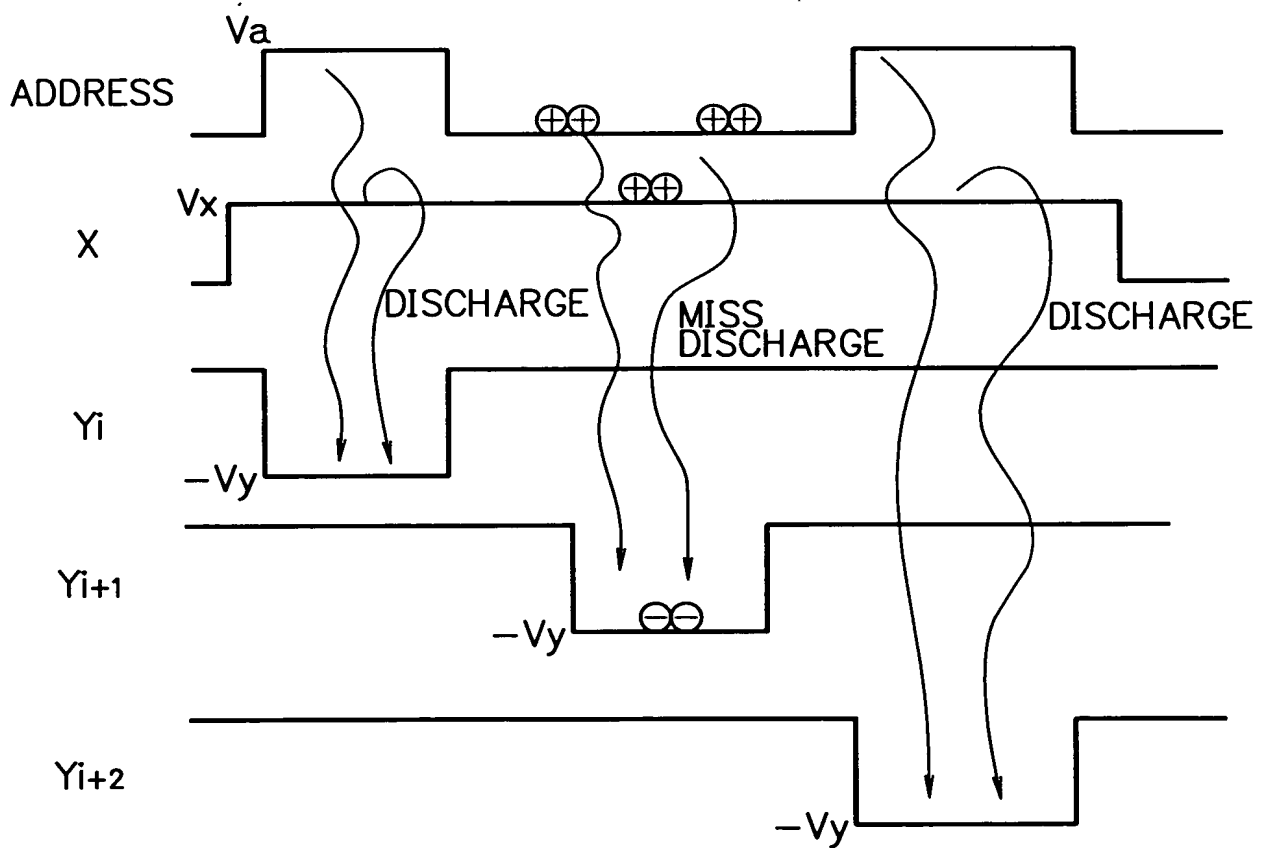
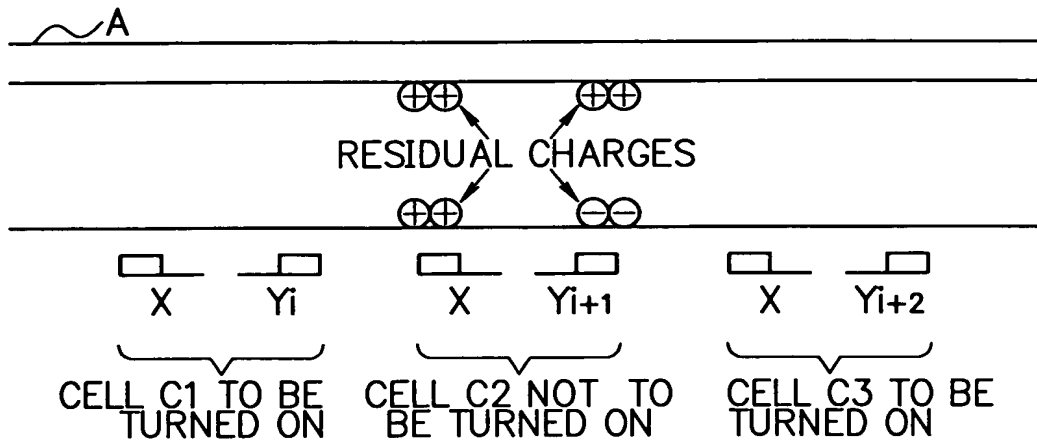


FIG. 7

CHARGE ACCUMULATION STATE IN ADDRESS PERIOD

<ADDRESS PERIOD>

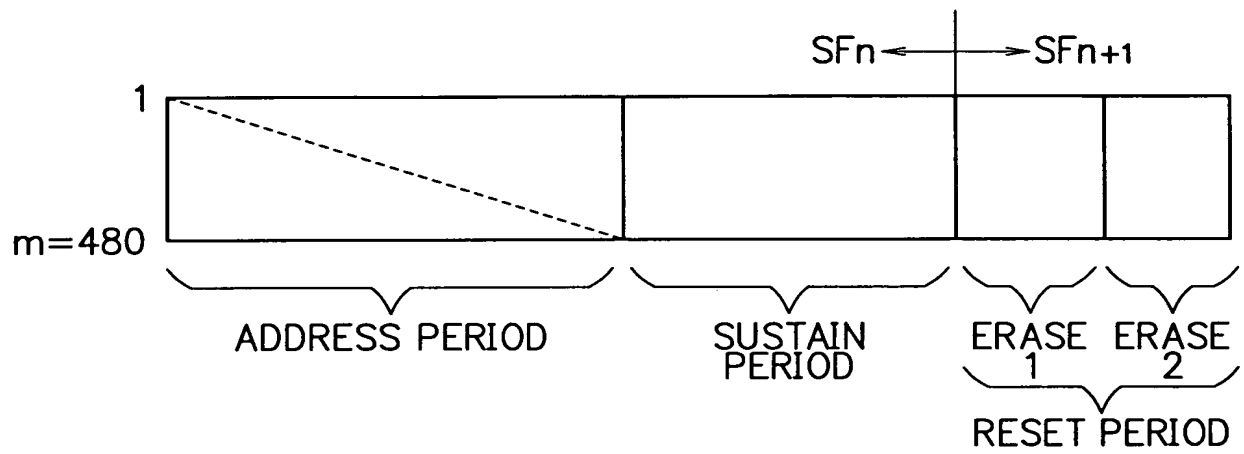


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FIG. 8

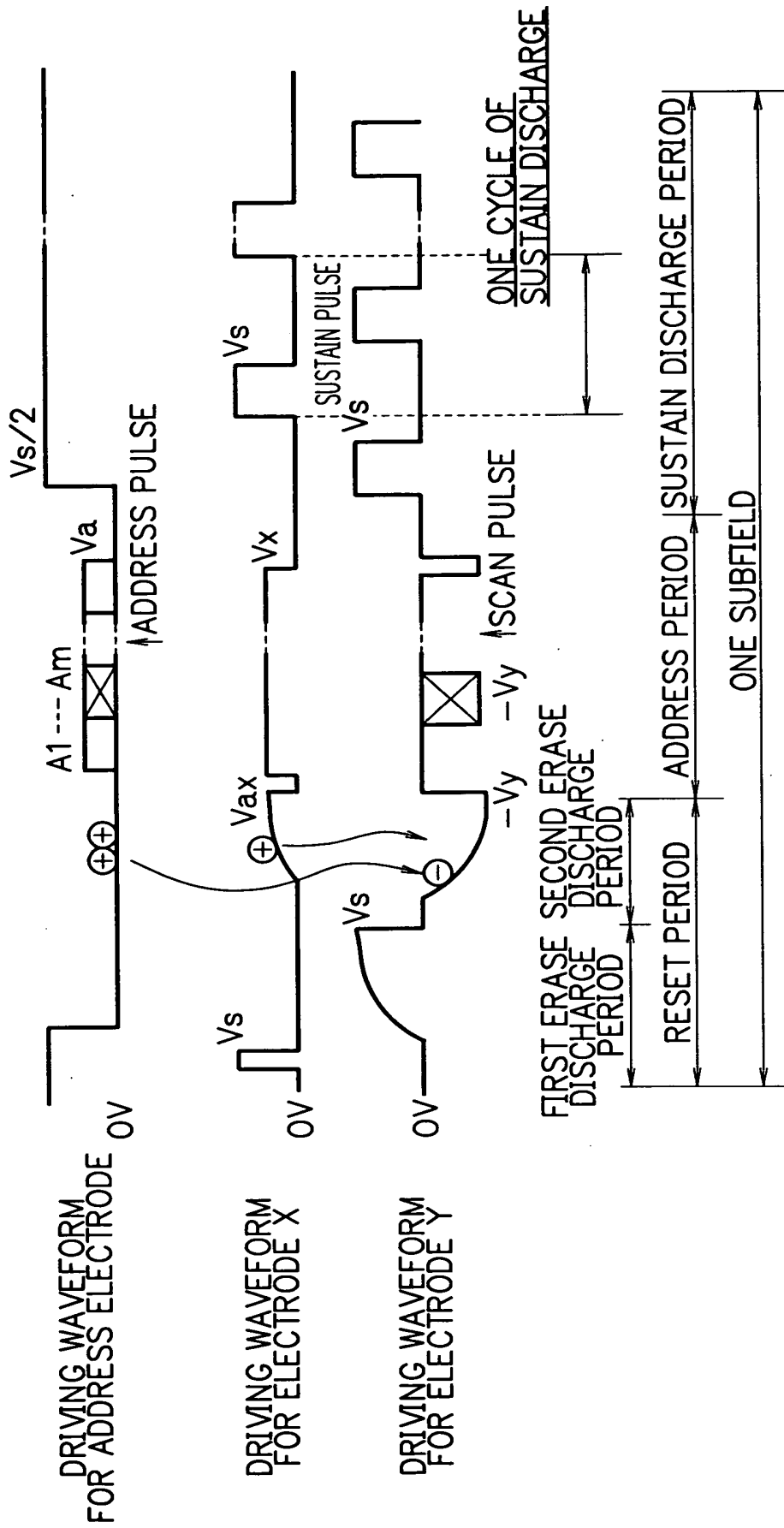
SUBFIELD STRUCTURE IN EMBODIMENT



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FIG. 9

PDP DRIVING WAVEFORM IN EMBODIMENT



APPROVED	O.G. FIG.	
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DRAFTSMAN		

ILLUSTRATION FOR VARIABLE V_{ax}

FIG. 10A

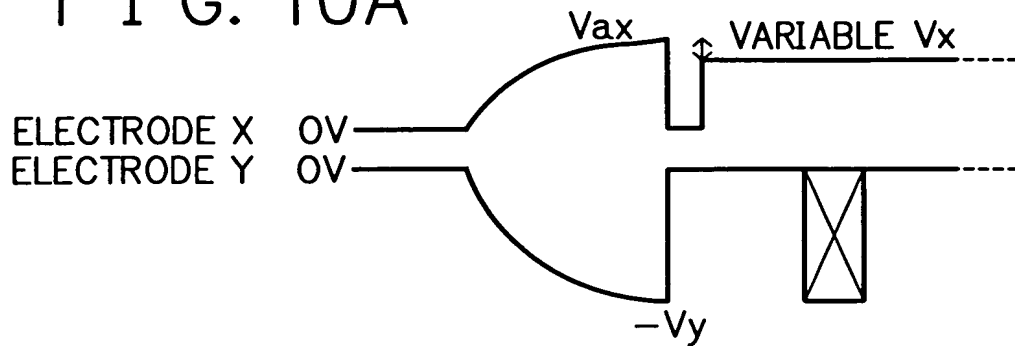


FIG. 10B

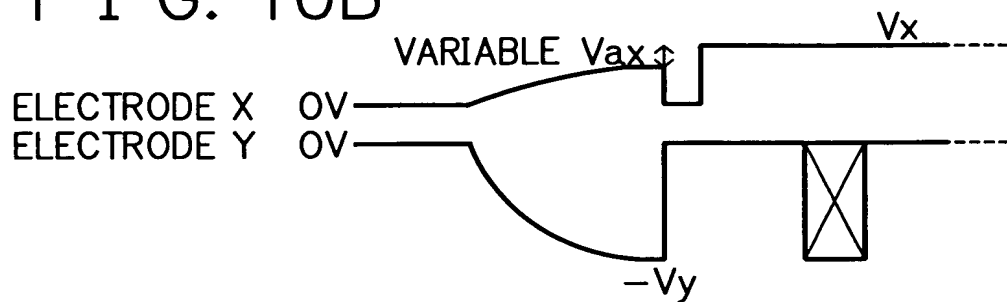
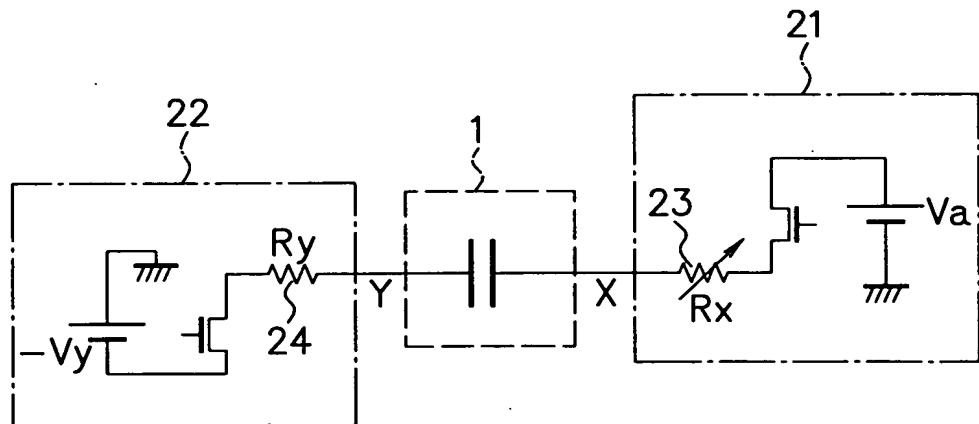


FIG. 11

HARDWARE ARRANGEMENT FOR VARIABLE V_{ax}



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CHARGE ACCUMULATION STATE IN EMBODIMENT

FIG. 12A

<END OF SUSTAIN>

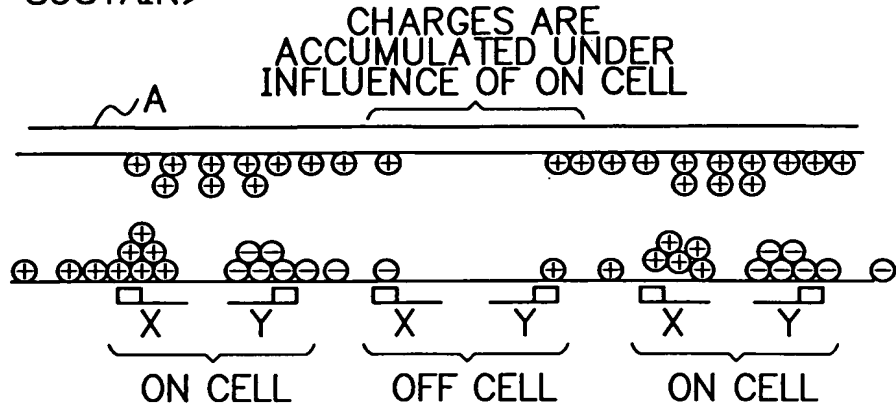


FIG. 12B

<ERASE>

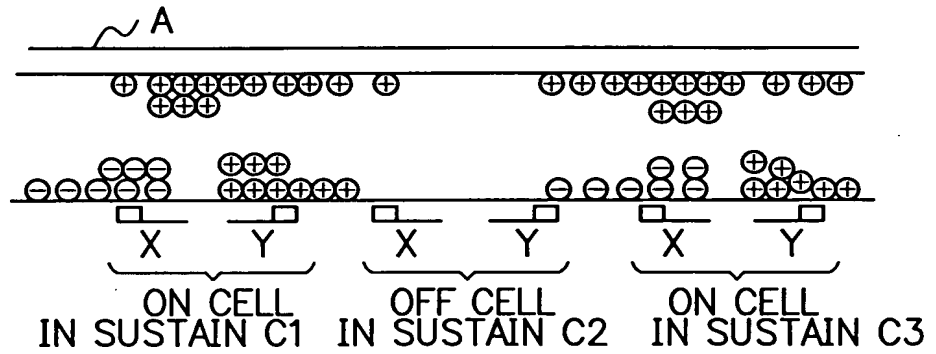


FIG. 12C

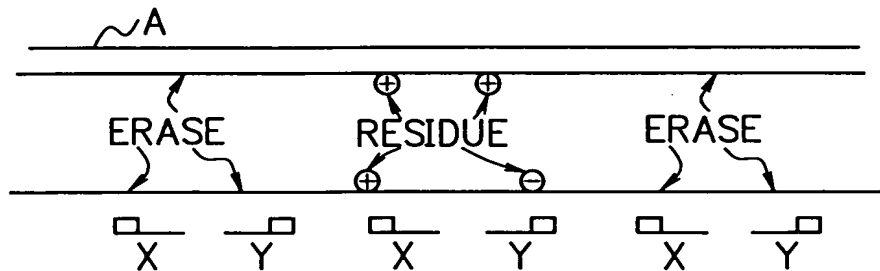
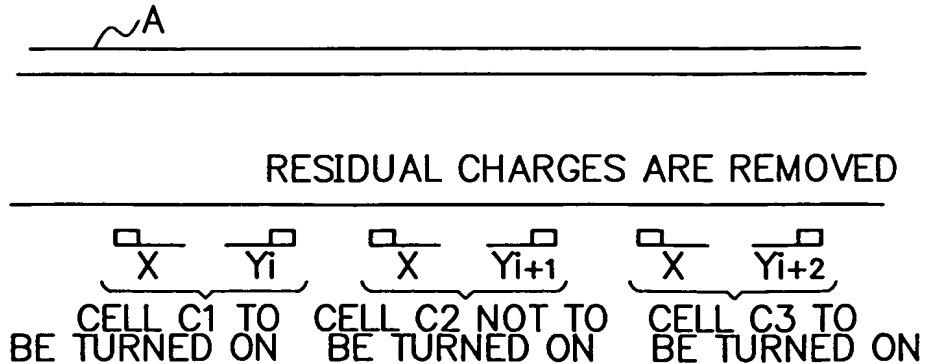


FIG. 12D



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FIG. 13

ANOTHER EXAMPLE OF PDP DRIVING WAVEFORM

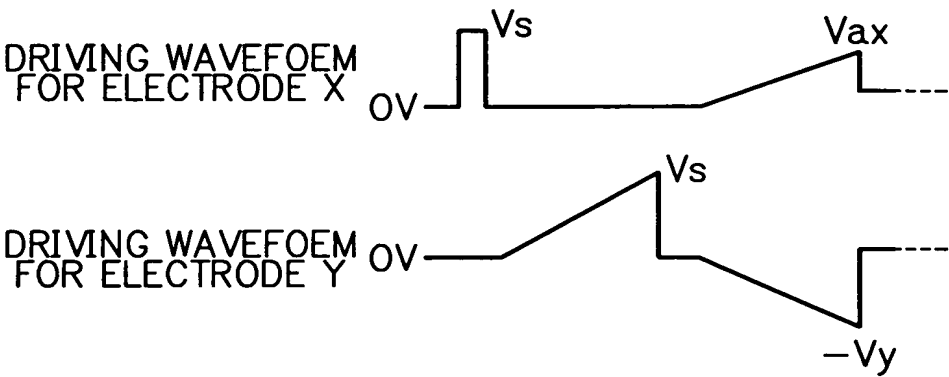


FIG. 14

ANOTHER EXAMPLE OF RISE TIMING OF
SECOND POSITIVE OBTUSE WAVE

